PDNO: 100204785-1 1 of 6
Inventor: Zarrieff et al.
Title: Spread Spectrum Clocking for
Data Transfer Bus Loading

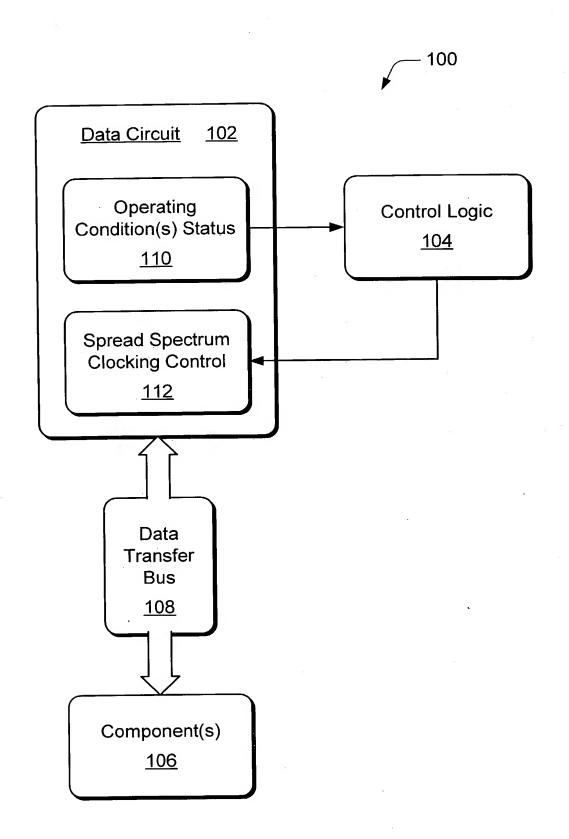


Fig. 1

Inventor: Zarrieff et al. Title: Spread Spectrum Clocking for **Data Transfer Bus Loading** 200 Processor **ASIC Processor** Bus 206 <u>202</u> <u> 208</u> **PVT Status Firmware** Register <u>204</u> <u>216</u> Spread Spectrum **Clocking Control** <u>218</u> Memory Memory Bus Bus 210(N) 210(1) Memory Memory Component(s) Component(s) 214 212

PDNO:

100204785-1

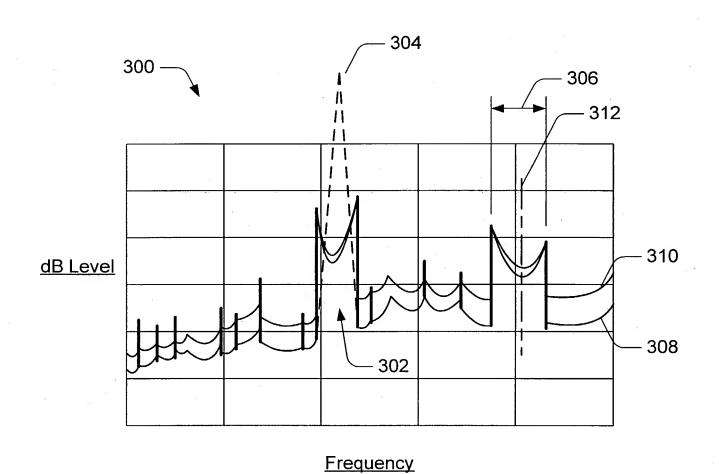
2 of 6

Fig. 2

PDNO: 3 of 6 100204785-1

Title: Spread Spectrum Clocking for

Inventor: Zarrieff et al. Data Transfer Bus Loading



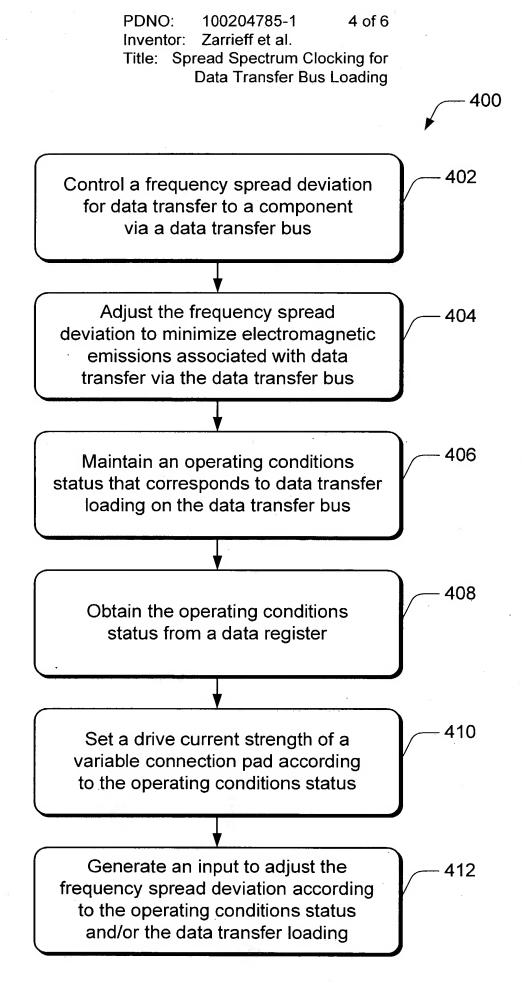


Fig. 4

PDNO: 100204785-1 5 of 6 Inventor: Zarrieff et al.

Title: Spread Spectrum Clocking for

**Data Transfer Bus Loading** 

**Printing Device** <u>500</u> Processor(s) Non-Volatile **Print Unit** Memory 508 520 <u>502</u> Operating System **RAM** Scan Unit <u>514</u> <u>504</u> <u>522</u> **Application** Program(s) UI / Menu Disk Drive <u>516</u> **Browser** <u>506</u> <u>524</u> Communication **ROM** <u>510</u> Display Panel Interface(s) <u>526</u> <u>518</u> Firmware <u>512</u>

PDNO: 100204785-1 6 of 6 Inventor: Zarrieff et al.

Title: Spread Spectrum Clocking for

Data Transfer Bus Loading

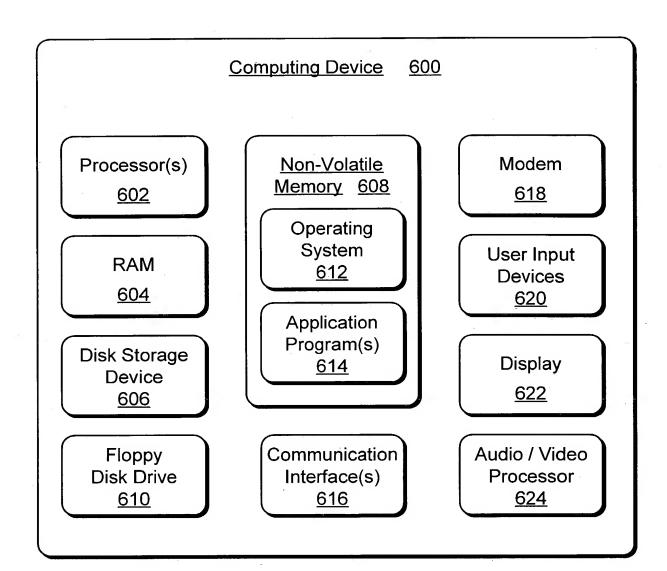


Fig. 6